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STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: Quintic Holdings	
Application No./Patent No.: 10/828,826	Filed/Issue Date: 04-21-2004
Titled: BIAS VOLTAGE CIRCUIT WITH ULTRA LOW OUTPUT IMPEDANCE	
Quintic Holdings, a Corpora	
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.	
states that it is:	
1.	
2. an assignee of less than the entire right, title, and interest in (The extent (by percentage) of its ownership interest is	%); or
3. the assignee of an undivided interest in the entirety of (a co	omplete assignment from one of the joint inventors was made)
the patent application/patent identified above, by virtue of either:	
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OR  R A chain of title from the inventor(s), of the patent application	n/patent identified above, to the current assignee as follows:
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